ABSTRACT

A display device is formed by a panel in which a display area and a peripheral circuit part for driving the display area are integrally formed in an integrated manner on an insulating substrate 1. The display area includes pixel electrodes arranged in a form of a matrix, a common electrode opposed to the pixel electrodes, and an electrooptic material retained between the pixel electrodes and the common electrode. The circuit part includes a driver for writing a signal voltage to a side of the pixel electrodes according to display data, a common driver 5 for applying a common voltage to a side of the common electrode, an offset circuit 51 having a coupling capacitor C1 for generating a predetermined offset voltage ΔV to adjust level of the common voltage with respect to the signal voltage, and a start circuit 52 for pre-charging the coupling capacitor C1 to an offset voltage ΔV at a time of a rising edge of power supply voltage, and discharging the coupling capacitor C1 at a time of a falling edge of the power supply voltage. Thus, the start circuit for the common driver is included within the display device of a system display configuration, so that mounting can be rationalized.